

## INVENTOR INFORMATION

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Inventor Two Given Name:: Toshimasa  
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## CORRESPONDENCE INFORMATION

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## APPLICATION INFORMATION

Title Line One:: Semiconductor Device And Method Of Manuf  
Title Line Two:: acturing The Same  
Total Drawing Sheets:: 12  
Formal Drawings?:: Yes  
Application Type:: Utility  
Docket Number:: 4041J-437DVA  
Secrecy Order in Parent Appl.?:: No

## CONTINUITY INFORMATION

This application is a:: DIVISION OF  
> Application One:: 09/340,020  
Filing Date:: 06-28-1999 *ABN*

## PRIOR FOREIGN APPLICATIONS